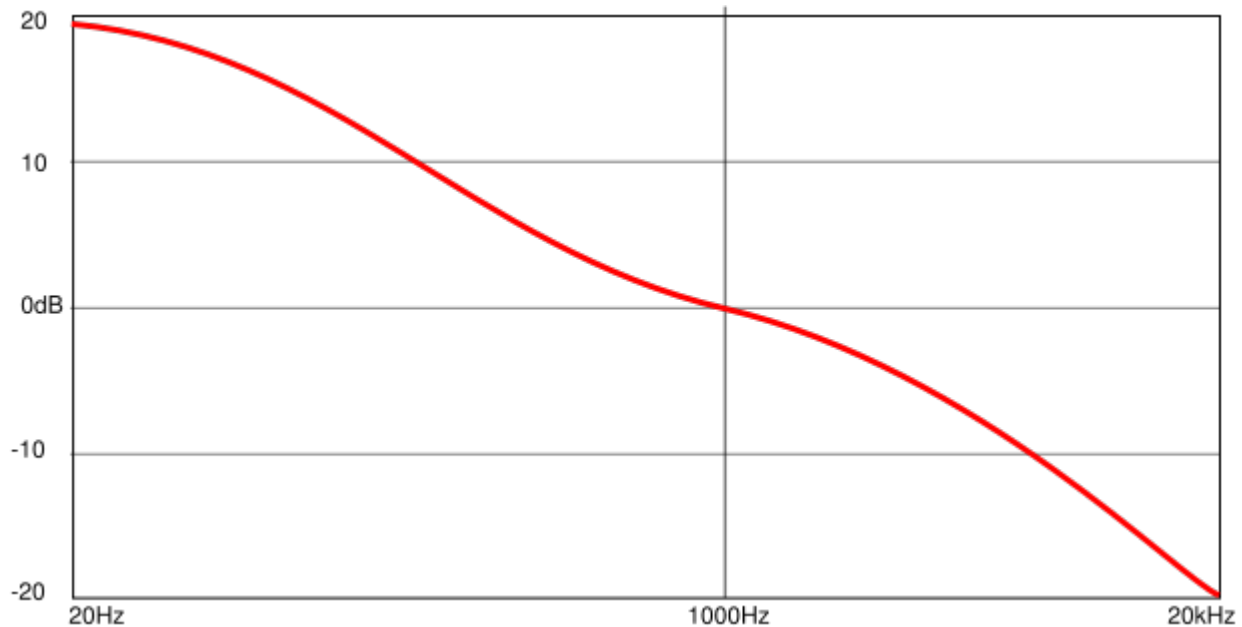


Goal: gain experience designing a circuit to fulfill a specific purpose

This project will demonstrate design skills to potential employers, and it's web log will bolster my portfolio.

Phonograph records are cut with lower frequencies attenuated and higher frequencies amplified to allow longer playtimes on the platter surface. To compensate when amplifying the signal to line level must follow this curve:



[1]

Further reading on RIAA equalization specification, read and understand the theory other hobbyists used in designing their compensation circuits.

Time line:

Clarify problem definition by 03/02/12

Meet with Mylène on 16/02/12

Setup web space for keeping track of project by 10/02/12

Decide on amp configuration by 22/02/12

Complete Multisim schematic and report by 01/03/12

Meet with Mylène on 01/03/12

Complete prototype by 5/04/12

Demonstrate the amp and explain its principals to the class by 12/04/12

Rubric: all portions are weighted equally, and are binary

Clearly define problem	$\frac{1}{1}$
Research solution strategies and decide my course of action	$\frac{1}{1}$
Design schematic and test in Multisim	$\frac{1}{1}$
Build functional mono prototype	$\frac{1}{1}$
Demonstrate amp and explain its function to the class	$\frac{1}{1}$
Web log	$\frac{1}{1}$
Total	$\frac{6}{6}$

Citation

[1] I Fergusson. (2006). *RIAA-EQ-Curve*[Online]. Available:
<http://commons.wikimedia.org/wiki/File:RIAA-EQ-Curve.svg>